

KIX0136-PCT

PATENT *S. Stevenson**pb 3*IN THE UNITED STATES PATENT AND TRADEMARK OFFICE *5-401*

In re the Application of:

SHINICHI SUZUKI ET AL.

U.S. Nat'l. Phase Appln.
of PCT/JP99/04624

ATTN. APPLICATION BRANCH

Int'l. Filing Date: 26 AUGUST 1999

For: SEMICONDUCTOR DEVICE AND SUBSTRATE
FOR MOUNTING THE SAMEAssistant Commissioner of Patents
Washington, D.C. 20231**INFORMATION DISCLOSURE STATEMENT**
ACCOMPANYING FILING

Sir:

In accordance with the requirements of 37 C.F.R. §§ 1.56, 1.96-1.97 and M.P.E.P. § 609, applicants, through their attorneys, hereby bring to the attention of the Examiner the references listed on the attached Form PTO 1449. Copies of the references are enclosed for the convenience of the Examiner.

The Japanese references are cited in the International Search Report and International Preliminary Examining Report of applicant's corresponding International Application. This Information Disclosure Statement is being filed concurrently with the U.S. national phase application.

Int'l. Appln. No. PCT/JP00/04624
Inventor: Shinichi SUZUKI et al.
Attorney's Docket No. KIX0136-PCT

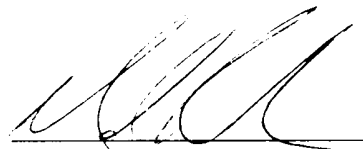
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The above information is presented so that the Patent and Trademark Office may, in the first instance, determine any materiality thereof to the claimed invention. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Respectfully submitted,

SHINICHI SUZUKI et al.

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